

RESUME

B. AJAY KUMAR

Department: Electronics and Communication Engineering

College: Seshadri Rao Gudlavalleru Engineering College

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Address for Communication

B.Ajay kumar,

S/o Srinivasa Rao,

Dr No: 1-761, Mandali Nagar,

Avanigadda,

Avanigadda (Mandal),

Krishna (District)-521121

CAREER OBJECTIVE:

To be in an environment where I can work up to my full potential, enhance my skills and become a good asset to the organization.

Academic Qualifications

Institution	Board/University	Examination	Year & Month	%Marks
JNT University Kakinada, Kakinada	JNTUK	Ph.D. (pursuing)	2020 September	-----
Andhra university college of engineering.	AU	M.E(Electronics instrumentation)	2013 November	8.95 (CGPA)
Sri Vasavi engineering College, Tadepalli gudem.	JNTUK	B.Tech (Electronics &communication Engineering).	2010 May	78.60
Narayana Jr college, Vijayawada.	Board of Intermediate Education.	Intermediate Public Examination.	2006 April	96.10
Sri vaninikethan, Koduru.	Board Of Secondary Education.	10th Class.	2004 April	90.00

***GATE -2011 Qualified (503 score)**

Professional Experience

S. No.	Designation	Institution Name	Working Period	
			From	To
1	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	16-06-2014	Till date
2	Assistant Professor	VIET narava, Visakhapatnam	01-10-2013	15-06-2014

Professional Body Membership -- IEI – AM 162859-6

NPTEL COURSES:

Sl. No.	Examination session	Subject(s) registered	Performance	Grade
			Total score	
1.	Jan-Apr 2018	Electromagnetic Theory	77	Elite (Topper 5%)
2.	Feb-Apr 2019	Electromagnetic Waves in Guided and Wireless Media	84	Elite + Silver (Topper 5%)
3.	Jul-Oct 2019	Digital Circuits	83	Elite + Silver (Topper 5%)
4.	Jan-Apr 2020	Microprocessors and Interfacing	97	Completed the course

Workshops /Conferences /Seminars Attended No.: 11

1. Attended a seminar on “**Advances in Communication, Navigation & Computer Networks (ACNCN-2012).**” held at Andhra University college of engineering on March 17th and 18th, 2012.
2. Attended a one day work shop on “**Research in Energy Management Wireless Networks & Smart Transportation system.**” organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada- 520 002 on 29th March 2014.
3. Attended a one day workshop on “**Speech Processing: Current Challenges And Hands-On Experience**”. On 23rd August, 2014 Organized by Signal Processing and communications Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College.

4. Attended a one day work shop on “**Research in Signal and Image Processing on FPGA’s and embedded flat forms.**” Organized by The Institution of Engineers (India), Vijayawada Local Centre, Vijayawada- 520 002 on 22nd November 2014.
5. Attended a one day workshop on “**Ultra Low Power Communication using CC430 Microcontroller: Hands on Experience**”. On 7th February, 2015 Organized by Signal Processing and communications Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College.
6. Participated in a two week ISTE STTP on “**Introduction to Design of Algorithms.**” Conducted by Indian Institute of Technology Kharagpur from 27th April, 2015 to 30th May, 2015.
7. Attended a one week refresher course on “**Microprocessors& Microcontrollers**” held during 17-21 November, 2105 at Gudlavalleru Engineering College organized by Research and Development Center, Department of ECE.
8. Participated in the faculty development programme (FDP) on “**Development of Wireless sensor networks using Network simulator (NS2) – Hands on experience**”. organized by Communication Research Group in Association with Vertex soft, Hyderabad on 1st -5th December 2015.
9. Participated in the Workshop on “**Design and simulation of Antennas & Microwave Devices using ANSYS HFSS**”. Under TEQIP-II, organized by Department of Electronics and Communication Engineering, SVECW in collaboration with ENTUPLE Technologies held from 3rd to 7th October, 2016.
10. Participated in one week faculty development programme (FDP) on Hands on Experience In “**signal processing applications**” organized by signal processing Research Group, Department of ECE, Gudlavalleru engineering college on 7th -12th November 2016.
11. Participated in a two week ISTE STTP on “**Cmos,Mixed signal and Radio frequency VLSI Design.**” Conducted by Indian Institute of Technology Kharagpur in association with Gudlavalleru Engineering college from 30th January to 4th February, 2017.
12. Attended a five day short term course on “**Trends in VLSI, Communications and Signal processing (TVCoS-2019)**” during 5th -9th march 2019 in the department of Electronics and Communication Engineering at National Institute of Technology Andhra Pradesh (NIT Andhra), Tadepalligudem.

Certifications/Training Programs Attended No. : 06

1. Attended a one day training program on Faculty Orientation Program conducted by **JNTUK centre of Excellence for e-Resource Development & Deployment (CoEeRD)** and Globarena Technologies Pvt.Ltd. held at LBCE on 22 August 2014.

2. Two day training program on “*Training Teachers for Excellence*”, organized by Gudlavalleru Engineering College, Gudlavalleru during 19th- 21st August 2014 and 29th - 31st December 2014.
3. Participated in a Two-Day Training program on “*RF circuit Design using ADS software: Hands on Experience.*” Under TEQIP-II, organized by Department of Electronics and Communication Engineering, SVECW in collaboration with Agilent Technologies held during 17-18 June 2015.
4. Two day training program on “*Digital Image Processing using MATLAB*” organized by Computer Centre, MIT Campus, Anna University, Chennai during 20th-21st June 2015.
5. Attended a Two day staff training program on “*Simulation of Linear & Digital Integrated Circuits, Microcontroller Based Applications using NI Multisim*”, held during 4th -5th November 2016 at Gudlavalleru Engineering College, Gudlavalleru.
6. Attended a Two day staff training program on “*Advances in VLSI Design and Embedded Systems*”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th November 2017.
7. Participated in a one week faculty development program on “*Communications and signal processing*”, organized by department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 29th October to 3rd November 2018.
8. Attended a Two day training program on “*Design and Development of Digital Circuits Using VLSI Tools* ”, Organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th-7th December 2018.

Industrial Training Programs: 02

1. Attended **one month** “Faculty industrial training program” on “**study and testing of RF designs**” from 4-june-2018 to 04-july-18 at Efftronics systems Pvt Ltd.,at Vijayawada.

Papers Published in Journals No : 4

1. **Bhupathi Ajay Kumar, S Aruna kumari**, published a paper on “**A Novel Scheduling Model for Fast Aggregation in Wireless Sensor Networks**”. In Global Journal of Advanced Engineering Technologies Volume 6, Issue 2-2017.ISSN (Online):2277-6370 & ISSN (Print): 2394-0921.

2. **B. Ajay Kumar**, V. Gopi, Published a paper on “*Channel hopping Algorithm in Cognitive Radio Networks*” in International Journal of Advanced Research in Computer and Communication Engineering ISO 3297:2007 Certified Vol. 5, Issue 8, August 2016.
3. K. Rushendra babu, **B. Ajay Kumar**, R. Chandrasekhar, Published a paper on “*Convergence Rate Improvement and Interference Reduction in Smart Antenna System.*” in **IJAREEIE**, Volume 5, Issue 2, February 2016.
4. P. Vidyullatha, R. Chandrasekhar, **B. Ajay Kumar**, Published a paper on “*Performance Analysis of PAPR in STBC MIMO-OFDM System under Different Modulation Schemes*” in International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 5, Issue 1, January 2016.

Papers Published in Conferences No : 2

1. **B. Ajay Kumar** “*Cooperative Spectrum Sensing in Cognitive Radio Networks using Fuzzy logic*” in the track of Communication of the “**6th International Conference on Innovations in Electronics and Communication Engineering (ICIECE-2017)**” held during 21st & 22nd July 2017.
2. R. Chandrasekhar, **B. Ajay Kumar**, K. Rushendra Babu, M. Kamaraju, “*Optimization of Peak to Average Power Ratio Reduction Using Novel Code for OFDM Systems*” Microelectronics, Electromagnetic and Telecommunications, Lecture Notes in Electrical Engineering 372(Springer), DOI 10.1007/978-81-322-2728-1_24, pp:267-275,2016.

Guest Lectures Delivered No. : 01

1. Delivered a lecture on “*GI-FI Technology*” at Gudlavalleru Engineering College, Gudlavalleru on 1st October 2014 for ECE Faculty.
2. Delivered a lecture on “*Analysis of RF design*” at Gudlavalleru Engineering College, Gudlavalleru on 28th march 2019 for ECE Faculty.

Subjects Handled

Under Graduation

- Electromagnetic field theory.
- Transmission lines and Wave Guides.
- Electro Magnetic field theory and Transmission lines.
- Microprocessor and Multi core systems.
- Probability Theory and stochastic process.

Labs Handled

Under Graduation

- Microprocessor and Microcontroller.
- Pulse and digital circuit lab.
- Digital Communications lab.

- Microwave and optical communication lab.

Other Responsibilities

College Level (in GEC) -- LCD Projectors in charge 2020 jan-2024 Dec
 GECFEST'15 (Guest receiving committee member)
 GECFEST'16 &17 (Discipline committee member)
 GECFEST'18 (Sports committee member)
 SRGEC-Silver Jubilee'24 (Discipline committee member)

Department Level (in GEC)

- Department Library Coordinator from 2014 june to 2020 july
- III, IV & M. Tech Examination Coordinator for the academic year 2015-2016.
- Working as Class in-charge for the academic year 2016-2017.
- Working as Class in-charge for the academic year 2017-2018.
- Working as Class in-charge for the academic year 2018-2019.
- Working as student counselor.
- Member of "E-Look" magazine for academic year 2015-2016.
- Staff meeting minutes in-charge.

NO. Of Projects Guided: UG level-05 PG level-03

Books Published No: 4

Books Published

1. Co-author for chapter 3.3 titled "Microprocessor and interfacing laboratory" Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016.
2. Co-author for chapter 4.2 titled "Microprocessors and microcontrollers laboratory "Electronics and Communication Engineering laboratory Manual series with ISBN – 978-1-4276-5563-9 in the year 2016.
3. Co-author for chapter 6 titled "Electromagnetic waves and transmission lines."Electronics and Communication Engineering Instructional Manual with ISBN – 978-1-4276-5539-4 in the year 2015.
4. Co-author for chapter 2.2 titled "Analog communication laboratory"Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2016.

R&D and Consultancy: 01

S. No.	Project Title	Source of Funding	Duration	Role	Status
1	Design and development of automated public	University Grants Commission	2 Years	Co-Investigator	On-going

	transportation system & traffic guidance system.				
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Curriculum Design and Development:

- Electro Magnetic field theory.
- Transmission lines and waveguides.
- Microprocessor and Multi core systems.
- Microprocessor and Micro controller Lab Manual.

Academic Rewards / Achievements:

- Stood **College First (state 8th rank)** in Inter First Year.
- I got the price money of **1000 Rs** for 2nd and 4th year **TOPPER**.
- Stood **College first** in B.Tech final academic year
- Stood class **first in 1st** semester of M.E.

Strengths:

- My **hard work** is my First and Best Strength.
- Good interpersonal skills and communication skills with Positive Attitude.
- Excellent **team building** and **motivating skills**.

Projects At Student Level:

M.E project:

Title: Slot Fair Scheduling For Real Time Applications On Uplink Of Wimax Networks.

B.Tech project:

Title: Access Control System By Using I-Button Technology.

Role: Team Leader.

Extra Activities at Student Level:

- Participated in **INFOSYS CAMPUS CONNECTS PROGRAM** at **SVEC**.
- I gave a paper presentation on “**Mobile communication**” for Microcosm’09, a National Level Technical Symposium by the Department of Electronics and Communication Engineering on 20th and 21st of March at Mahatma Gandhi Institute of Technology.

Extra Curricular Activities:

- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.
- NBA Criteria- IV and VII (Faculty Contributions) member

Declaration

I, hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 31-01-2024

Place: Gudlavalleru

Signature

(B.AJAY KUMAR)